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(71) Applicant (for all designated States except US): TELECOM ITALIA S.P.A. [IT/IT]; Piazza degli Affari, 2, I-20123 Milano (IT).

(72) Inventors; and

(75) Inventors/Applicants (for US only): BRAGAGNINI, Andrea [IT/IT]; Telecom Italia S.p.A, Via Reiss Romoli, 274, I-10148 Torino (IT). TUROLLA, Maura [IT/IT]; Telecom Italia S.p.A., Via Reiss Romoli, 274, I-10148 Torino (IT).

(74) Agents: GIANNESI, Pier, Giovanni et al.; Pirelli S.p.A., Viale Sarca, 222, I-20126 Milan (IT).

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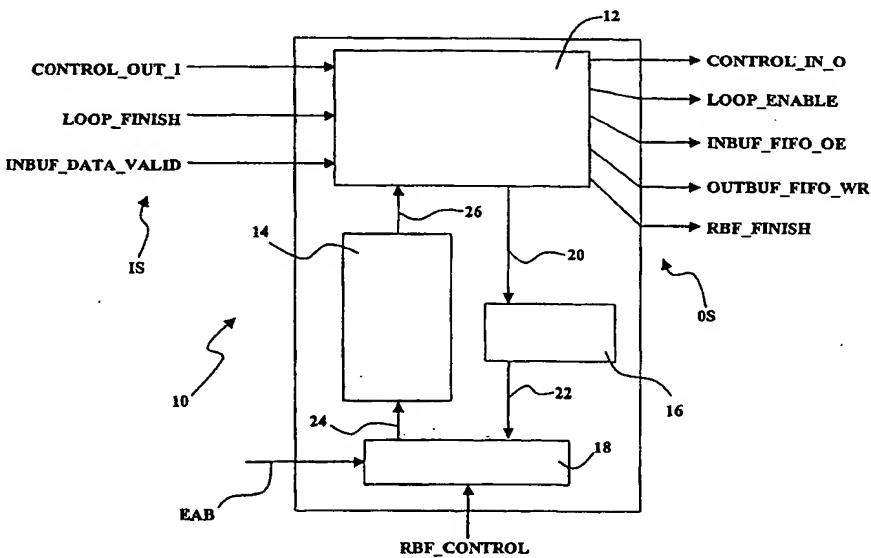
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(54) Title: A RECONFIGURABLE STATE MACHINE ARCHITECTURE AND RELATED METHOD OF EXECUTION



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(57) Abstract: An architecture for a state machine (10) with a number of states of the machine, comprises a memory (14) having a set of addresses. The memory (14) is arranged to store at each of said addresses in the set the complete description of a respective one of said number of states of the machine (10).